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Stack management unit and method for a processor having a stack
US Pat. 6038643 - Filed Jan 23, 1997 - Sun Microsystems, Inc.
4A) for a method are not located on stack cache 155. ... stores a line of zeros
in data cache 165 and marks that line 60 of data cache 165 as dirty. ...

System and method for swapping blocks of tagged stack entries between a ...
US Pat. 5893121 - Filed Apr 23, 1997 - Sun Microsystems, Inc.
In the preferred embodiment described above, a writeback cache with no dirty bits
is assumed. However, if the stack cache is designed to be a write through ...

Hardware virtual machine instruction processor
US Pat. 6021469 - Filed Jan 23, 1997 - Sun Microsystems, Inc.
4A) and local variable storage 421 that are maintained in stack cache 155 ...
of the register is saved to stack 400, ie, whether the register is dirty. ...

Superscalar microprocessor stack structure for judging validity of predicted ...
US Pat. 5968169 - Filed Sep 25, 1997 - Advanced Micro Devices, Inc.
The stack cache will drive the operand bus, but the reservation station knows to
... request to flush the stack cache; the stack cache begins writing dirty ...

Superscalar microprocessor employing a way prediction unit to predict the ...
US Pat. 5764946 - Filed Apr 8, 1997 - Advanced Micro Devices
SCDC — stack cache and data cache are transfer a line between them. ... request to
flush me stack cache; the stack cache begins writing dirty lines back to ...

Load/store unit with multiple oldest outstanding instruction pointers for ...
US Pat. 5887152 - Filed Apr 12, 1995 - Advanced Micro Devices, Inc.
The stack cache will drive the operand bus, but the reservation station knows to
... request to flush the stack cache; the stack cache begins writing dirty ...

Process of executing a method on a stack-based processor
US Pat. 6125439 - Filed Jan 23, 1997 - Sun Microsystems, Inc.
4A) and local variable storage 421 that are maintained in stack cache 155 ...
of the register is saved to stack 400, ie, whether the register is dirty. ...

Processor with accelerated array access bounds checking
US Pat. 6014723 - Filed Jan 23, 1997 - Sun Microsystems, Inc.
10 15 25 The hardware organization of stack cache 155 is such that, ... content of
the register is saved to stack 400, ie, whether the register is dirty. ...

Method for reducing access to main memory using a stack cache
US Pat. 7065613 - Filed Jun 6, 2003 - Maxtor Corporation
More specifically, a write-back, line-oriented, stack cache structure is used.
The cache structure includes both a single 5 dirty flag and one address tag ...

Lookahead register value generator and a superscalar microprocessor ...
US Pat. 5768610 - Filed Jun 7, 1995 - Advanced Micro Devices, Inc.
SCDC — stack cache and data cache are transfer a line between them. ... request to
flush the stack cache; the stack cache begins writing dirty lines back to ...

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